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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i> | Application Number | | 10682468 |
| | Filing Date | | 2004-04-12 |
| | First Named Inventor | | Mitchell Alsup |
| | Art Unit | | 2183 |
| | Examiner Name | | George D. Zalepa |
| | Attorney Docket Number | | 5500-92000 |

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| Examiner Initial* | Cite No | Patent Number | Kind Code† | Issue Date | Name of Patentee or Applicant of cited Document | Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear |
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| 6 | Hinton, G., et al., "A 0.18-MUM CMOS IA-32 Processor with a 4-GHZ Integer Execution Unit," IEEE Journal of Solid-State Circuits, Vol. 36, No. 11, November 2001, pages. 1617-1627. | <input type="checkbox"/> |
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See attached certification statement.

Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

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| Signature | /Robert C. Kowert/ | Date (YYYY-MM-DD) | 2006-11-16 |
| Name/Print | Robert C. Kowert | Registration Number | 39,255 |

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